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HIGH SENSITIVITY DEMODULATOR
FOR A RADIO TAG AND METHOD

TECHNICAL FIELD OF THE INVENTION

This invention relates generally to radio frequency identification (RFID) systems and more particularly to a high sensitivity demodulator for a radio tag and method.

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BACKGROUND OF THE INVENTION

5 The management and tracking of personnel, assets,
and other objects is required in a wide variety of
environments and is often cumbersome, labor intensive,
and expensive. Radio receivers and transmitters have
10 been used for many years to identify personnel and
objects in such environments. For example, many systems
are known for attaching radio tags to items, such as
automobiles, so that when automobiles equipped with radio
tags enter a certain area, such as a toll booth area, the
15 automobiles are automatically identified and the
appropriate tolls are deducted from corresponding
accounts, thereby obviating the need for drivers to stop
and make payment at toll booths. Innumerable other
applications for such radio tag systems have been
20 identified, in areas ranging from inventory control to
facility security to sporting event timing.

Radio frequency identification (RFID) systems
generally use a fixed position base station capable of
25 reading remote, portable tags attached to personnel,
assets, or other objects. Tags generally operate on
their wake-up frequency at low frequency, very low
frequency, or microwave bands. Because of power
consumption concerns and the life span of the portable,
remote radio tags operating at low frequency or very low
frequency, eXclusive OR ("XOR") gates are almost
exclusively used to demodulate incoming radio frequency
signals. The use of XOR gates leads to low sensitivity
and limited operational range for the radio tags.

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SUMMARY OF THE INVENTION

In accordance with the present invention, a high sensitivity demodulator for a radio tag and method is provided which substantially eliminates or reduces disadvantages and problems associated with conventional demodulators for radio tags.

According to one embodiment of the present invention, there is provided a radio frequency tag comprising a threshold voltage generator and a comparator for demodulating a received signal. The threshold voltage generator generates a threshold voltage signal and forwards that signal to a first comparator input. An antenna receives a signal and forwards that received signal to a second comparator input. The comparator generates a digital output signal based on the results of the comparison of the two inputs.

More particularly, in accordance with a particular embodiment of the present invention, the threshold voltage generator and comparator are powered by only leakage current from a local power supply. The threshold voltage generator is comprised of a first resistor and a second resistor connected in series between the local power supply and a ground. A threshold voltage generator output is connected to the junction between the first resistor and the second resistor to provide the threshold voltage signal to the comparator.

Technical advantages of the present invention include providing an improved radio tag. In particular, the radio tag utilizes a highly efficient comparator that can demodulate low powered analog carrier signals while drawing only minimal current. As a result of the ability to demodulate low powered signals, the radio tag has improved sensitivity and the operational range of the

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radio tag is significantly increased. In addition, power consumption is minimized to maintain long tag life.

Other technical advantages will be readily apparent to one skilled in the art from the following figures, description, and claims.

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BRIEF DESCRIPTION OF THE DRAWINGS

For a more complete understanding of the present invention and the advantages thereof, reference is now made to the following description taken in conjunction with the accompanying drawings, wherein like reference numbers are present like parts, and in which:

FIGURE 1 is a block diagram illustrating an RFID system in accordance with one embodiment of the present invention;

FIGURE 2 is a block schematic diagram illustrating the high sensitivity demodulator for the radio tag of FIGURE 1;

FIGURE 3 is a block schematic diagram illustrating a dual speed comparator that may be modified for use in the present invention; and

FIGURE 4 is a flow diagram illustrating a method for receiving and demodulating a radio frequency signal in accordance with one embodiment of the present invention.

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DETAILED DESCRIPTION OF THE INVENTION

FIGURE 1 illustrates a radio frequency identification (RFID) system 5 in accordance with one embodiment of the present invention. In this embodiment, the base unit 10 resides in a fixed location and communicates with an active radio tag 30 on an analog signal at a specified radio frequency. The radio tag 30 is a remote, portable, self-contained device that may be affixed to a moveable item, such as a person, inventory, or vehicle.

Referring to FIGURE 1, the base unit 10 is a conventional unit and includes a base unit antenna 12, a base unit transmitter 14, a base unit receiver 16, and a base unit processor 18. Base unit antenna 12 transmits and receives specific radio frequencies to allow communications between base unit 10 and radio tag 30. Base unit receiver 16 acquires incoming signals from base unit antenna 12 and demodulates the incoming signal. Base unit receiver 16 forwards the demodulated signal to base unit processor 18 to determine an appropriate action or response to the incoming signal.

Base unit transmitter 14 modulates signals generated by base unit processor 18 onto a carrier wave and transmits the modulated signal through base unit antenna 12 as the analog signal 20. The communicated signals may be initiated by base unit 10 in response to a signal received from the tag 30. As analog signal 20 travels away from base unit 10, the radio waves in analog signal 20 diminish in amplitude and, thus, in power. The remote, portable radio tag 30 has an operational range limited to the distance at which the transmitted radio waves fall below the minimum power level that radio tag 30 can detect.

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For an RFID application, the frequency of the carrier wave is preferably a low frequency (LF) or very low frequency (VLF). In an exemplary embodiment, a low frequency of 132 KHz is used for communications between base unit 10 and one or more radio tags 30. In this embodiment, base unit 10 utilizes amplitude modulation to carry the communicated signal on the carrier wave. It will be understood that other suitable modulation schemes may be used.

Radio tag 30 is active and portable RFID or other suitable tag that can be affixed to and identify a moveable object such as a person, a vehicle, or a piece of inventory. Active radio tags are radio tags which contain a local, self-contained power supply for providing power to internal components. Radio tag 30 is portable in that it can be easily transported by attachment to a person or a moveable object and remains operational during transport.

Radio tag 30 includes an internal tag antenna 32, a tag transmitter 34, a tag receiver 36, a tag processor 38, and a local power supply 40. Tag antenna 32 allows radio tag 30 to transmit and receive radio frequency signals. The radio frequency on which radio tag 30 operates is synchronized with the radio frequency of base unit 10 such that radio tag 30 and base unit 10 may communicate. In one embodiment, radio tag 30 and base unit 10 may each transmit and receive at the same frequency. In the exemplary embodiment, for example, both communicate at a frequency of 132 KHz.

Alternatively, radio tag 30 may receive signals on a first frequency and transmit signals on a second, discrete frequency in order to avoid collisions between the signals transmitted by different sources such as base unit 10 and radio tag 30.

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5 The tag receiver 36 is coupled to tag antenna 32 to receive signals collected by tag antenna 32. Tag receiver 36 includes a tuner circuit 42 that is tuned to the transmitting frequency of base unit 10 and a demodulator 44.

10 Tuner circuit 42 eliminates extraneous signals which may cause false activations of radio tag 30 by filtering frequencies other than a target frequency. Tuner circuit 42 may include conventional tuner circuitry such as a high pass filter comprised of a capacitor in series with a resistor connected to a ground. Tuner circuit 42 effectively narrows the bandwidth of the signal received and forwarded by tag antenna 32. In an exemplary embodiment, tuner circuit 42 is set to 132 KHz but may be set to any suitable frequency.

15 After tuner circuit 42 obtains and filters the received signal, the tuned and filtered signal is forwarded to demodulator 44. Demodulator 44 demodulates the received signal and extracts the communicated signal in the form of a square wave, or digital bit stream, for processing by tag processor 38. As described in more detail below, demodulator 44 has a high sensitivity so that it can detect and demodulate low powered signals. In one embodiment, the demodulator has a sensitivity of less than 10 millivolts. As a result, the radio tag 30 can detect and process weak signals which significantly increases the operational range of radio tag 30 to 150 feet and beyond.

25 After demodulator 44 extracts the communicated signal from the carrier wave, the communicated signal is forwarded to tag processor 38 for processing. Tag processor 38 may be any suitable general purpose processor, micro-processor, or micro-controller. Tag processor 38 receives the communicated signal and

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determines if a response is necessary. If tag processor 38 determines that a response should be sent to base unit 10, a tag transmitter 34 modulates the response signal from tag processor 38 and transmits the modulated signal to base unit 10 via tag antenna 32.

Before tag processor 38 will process an incoming signal, a wake up signal signature must typically be received. This allows the tag processor 38 to remain in a low power, or sleep, state during periods of inactivity when the radio tag is outside the range of the base unit 10 or is otherwise not communicating. The wake up signal signature informs tag processor 38 that information requiring processing will arrive immediately following the wake up signal signature.

The wake up signal signature may also be used to eliminate false activations of radio tag 30. False activations may occur when utilizing a low threshold voltage level in demodulator 44. False activations of radio tag 30 are eliminated in part by tuner circuit 42 that only allows signals at a target frequency to pass into demodulator 44. By utilizing a wake up signal signature, the remainder of potential false activations may be effectively eliminated or reduced while still providing a high sensitivity demodulator.

Radio tag 30 is powered by a local power source 40. Local power source 40 may be a battery, solar cell system, or other suitable portable power source. In an exemplary embodiment, local power source 40 is preferably a three volt lithium coin cell battery. A characteristic of any battery or power source connected to electrical components is the leakage of a certain quantity of electrical current into the electrical circuit. To maintain radio tag 30 life, demodulator 44 is configured to operate on only leakage current of local power source

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40. For the lithium coin cell battery, leakage current is generally less than four microamps. In this embodiment, the demodulator 44 may operate on about three microamps. By operating the demodulator 44 on leakage current only, radio tag 30 obtains a long shelf life of about 10 years before replacement of local power source 40 is necessary.

FIGURE 2 illustrates details of demodulator 44 in accordance with one embodiment of the present invention. The demodulator 44 includes a comparator 52 and a threshold voltage generator 54. The threshold voltage generator 54 generates a threshold voltage signal that is used by the comparator 52 to demodulate an incoming signal and determine if a communicated signal is present. Comparator 52 should be in an active state at all times so that it is prepared to sample any signal that may be obtained by tag antenna 32. Since comparator 52 is always active, threshold voltage generator 54 is always active and both continuously draw current from battery 40. The comparator provides high sensitivity at low power consumption levels and this increases the range of the radio tag 30 without limiting its effective life.

Comparator 52 has a first input 50, a second input 56, and an output 58. The first input 50 is coupled to the tuner circuit 42 after the analog signal 20 has been tuned and filtered to receive the tuned signal. The second input 56 is coupled to the threshold voltage generator 54 to receive the threshold voltage signal. The output 58 is coupled to tag processor 38.

The comparator 52 is connected to local power source 40 and preferably operates on leakage current of the local power source 40. In one embodiment, the link between local power source 40 and comparator 52 has a capacitor 68 for regulation of the electrical current

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supplied to comparator 52. This prevents fluctuations in local power source 40 current from adversely affecting operation of the comparator 52.

5 In operation, comparator 52 demodulates the tuned signal by comparing it to the threshold voltage signal generated by threshold voltage generator 54. The digital output on output 58 is based on the results of the comparison. If the tuned signal exceeds the threshold voltage signal, comparator 52 generates a high output on digital signal 58. If tuned signal 50 is less than threshold voltage signal 56, comparator 52 generates a low output on digital signal 58. A high output corresponds to an "on" bit, and a low output corresponds to an "off" bit. Tag processor 38 receives the digital signal on output 58 and determines an appropriate response to be transmitted to base unit 10.

10 The propagation delay of comparator 52 determines the sampling rate of the incoming signal. The propagation delay of comparator 52 is dependent on the specific frequency which must be captured, demodulated, and processed. Generally, a propagation delay of seven to ten times less than the period of the carrier wave being processed is sufficient to fully capture the communicated signal. Thus, seven to ten samples would be taken of the incoming signal during each period. In the exemplary embodiment where the carrier wave has a frequency of 132 KHz, the comparator 52 has a period of 7.6 microseconds and a propagation delay of 900 nanoseconds which yields approximately eight samples per cycle of the carrier wave. The tag processor 38 accumulates and interprets the samples.

20 Conventional comparators satisfy the low power requirements but are impractical for application to an RFID tag as the propagation delay at low overdrive

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signals (i.e. 10mv) is in excess of 12 microseconds. As described below, the comparator 52 of the present invention may be implemented by novelly modifying a commercially available system or fabricating the novel comparator.

FIGURE 3 illustrates an implementation of the comparator 52 in accordance with one embodiment of the present invention. In the embodiment, the comparator 52 is implemented by modifying a commercially available dual speed comparator to yield a sufficient propagation delay. In a particular embodiment, the dual speed comparator is a MAX975 operational amplifier configured as a comparator and manufactured by Maxim Integrated Products of Sunnyvale, California.

Referring to FIGURE 3, the dual speed comparator incorporates a standby mode of operation. The device incorporates two internal operational amplifiers (op-amps) 70 and 72 configured to function as individual comparators. The first op-amp 70 operates at a high speed, and the second op-amp 72 operates at low power and low speed. Each of the op-amps 70 and 72 are turned on and off via a timing circuit 74 through an OR Gate 76.

The dual speed comparator can be configured to function as follows: By holding the LP (Low Power) pin 78 at a high value, the high speed op-amp 70 is disabled and the low power, lower speed op-amp 72 is enabled. The STO pin 80 is tied to ground via a .1uf capacitor (not expressly shown) to disable auto-standby mode. The VCC pin 82 and ground pin 84 are tied to the local power supply 40 (lithium coin cell). The IN+ pin 86 is connected directly to the tag antenna 32 at the output of tuner circuit 42. The IN- pin 88 is connected directly to the output of the threshold voltage generator 54 as well as bypassed with a .1uf capacitor (not expressly

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shown) to ground to stabilize the signal and prevent switching noise. With the device configured this way, approximately 75% of the functional characteristics of the device are disabled and not utilized. This is done in order to take advantage of the internal operational amplifier 72 being produced in the same CMOS process as the high speed op-amp 70 and thus inheriting some of the same performance characteristics. Operational amplifier 72 can then be applied to the present invention as a comparator with a low propagation delay of 600ns at 10mv overdrive with a current draw of three microamps. As previously described, comparator 52 used as a demodulator in radio tag 30 should have a propagation delay of 900 nanoseconds, considerably less than the 12 microseconds available from a commercially available comparator.

In another embodiment, comparator 52 may be implemented in an application specific integrated circuit (ASIC). In this embodiment, the comparator 52 may have a conventional design but be fabricated using complimentary metal oxide semiconductor (CMOS) techniques at a sub .35 micron process, such as Hewlett-Packard's MOSES process. In this embodiment, comparator 52 has a typical propagation delay of 900 nanoseconds and a maximum propagation delay of one microsecond at less than 10 millivolts overdrive using two to three microamps of current from a local power supply 40 (lithium coin cell battery) over an extended temperature range with CMOS compatible outputs on a .35 micron process. Comparator 52 is implemented using a low power, sub-micron CMOS process to properly balance the tradeoffs between power consumption and performance that currently available comparators inadequately address.

An exemplary embodiment utilizes a comparator with a propagation delay of 900 nanoseconds and a power

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consumption between two and three microamps. Since threshold voltage generator 54 draws approximately 997 nanoamps and comparator 52 draws approximately three microamps, the total power consumption of demodulator 44 is less than four microamps which will yield an adequate shelf life and is less than the leakage current of local power source 40.

The threshold voltage generator 54 may be implemented as a voltage divider circuit. In this embodiment, the threshold voltage generator 54 has a first resistor 60, a second resistor 62 and a capacitor 66. A first resistor 60 is coupled to local power source 40. First resistor 60 should have a resistance high enough to minimize the leakage current flow while still providing sufficient leakage current to generate threshold voltage signal 56. If the resistance of first resistor 60 is too high, insufficient current will be available to generate threshold voltage signal 56. If the resistance of first resistor 60 is too low, excessive current will be drawn from local power source 40 and inadequate shelf life will result.

A second resistor 62 is placed in series with first resistor 60 and is connected to a ground 64. The resistance of second resistor 62 is chosen to generate the appropriate voltage for threshold voltage signal 56. By varying the resistance of second resistor 62, the sensitivity of demodulator 44 may be varied. An increase in the resistance of second resistor 62 results in lower sensitivity of demodulator 44.

Capacitor 66 is connected across second resistor 62 in order to provide a constant voltage on threshold voltage signal 56. Capacitor 66 insures that the sensitivity of comparator 52 is constant and does not fluctuate with any fluctuations in local power source 40.

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In the exemplary embodiment, a three megaohm resistor is used as first resistor 60 and a 6.8 kilohm resistor is used as second resistor 62. The total current drawn by threshold voltage generator 54 is $I=E/R$, where I is current in amps, E is energy in volts, and R is resistance in ohms. Utilizing a three volt local power source 40 and having a total resistance from the resistances of first resistor 60 and second resistor 62 of 3,006,800 ohms, the total current drawn by the threshold voltage generator 54 is $3V/3,006,800 \text{ ohms} = 997$ nanoamps. Therefore, threshold voltage generator 54 draws less than 1 microamp from battery 44.

In this embodiment, the threshold voltage signal may be calculated as $E(R2)/(R1+R2)$, which is $3V(6800 \text{ ohms})/(3,006,800 \text{ ohms})$, or 6.8 millivolts. Therefore, any received signal which exceeds 6.8 millivolts may be processed.

FIGURE 4 illustrates a method for receiving and processing a radio frequency signal in the radio tag 30. Referring to FIGURE 4, the method begins at step 102 where tag antenna 32 receives an analog signal which may contain either a modulated communicated signal from base unit 10 or general band noise occurring on the target frequency. At step 104, tuner circuit 42 filters and tunes analog signal 20. At step 106, threshold voltage generator 54 generates a threshold voltage signal 56 for use by comparator 52 in determining if the tuned signal represents incoming information. As previously described, the threshold voltage signal is constantly generated.

Next, at decisional step 108, comparator 52 compares the tuned signal to the threshold voltage signal. If the tuned signal does not exceed the threshold voltage

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signal, the NO branch of decisional step 108 leads to step 112 where comparator 52 generates a low output 58.

Returning to decisional step 108, if the tuned signal exceeds the threshold voltage signal, the YES
5 branch of decisional step 108 leads to step 110 where comparator 52 generates a high output 58. Steps 110 and 112 lead to step 114 where the tag processor 38 processes the digital signals. Recall that tag processor 38 may need to accumulate several samples before determining if
10 a bit is on or off.

It is apparent that there has been provided in accordance with the present invention a high sensitivity demodulator for a radio tag and method which satisfies the advantages set forth above such as operation on
15 leakage current from a local power source, significantly higher sensitivity, and significantly increased operational range. Although the present invention and its advantages have been described in detail, it should be understood that various changes, substitutions, and
20 alterations readily apparent to those skilled in the art may be made without departing from the spirit and the scope of the present invention as defined by the following claims.